

J.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.		Serial No.			
						M-3240-3D US		Not yet assigned			
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						Filing Date		Group			
					Herewith		2661	110			
			U.S. 1	Patent Documents							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing If Appro				
illitiai	AA	4,814,980	3/1989	Peterson et al.	395	200.46	Пирри	·			
	AB	4,933,836	6/1990	Tulpule et al.	395	800.12					
	AC	5,181,017	1/1993	Frey, Jr. et al.	340	825.02					
	AD	5,253,248	10/1993	Dravida et al.	370	228					
	AE	5,339,396	8/1994	Muramatsu et al.	395	312	1				
	AF	5,377,333	12/1994	Nakagoshi et al.	395	312					
	AG	5,471,623	11/1995	Napolitano, Jr.	395	200.03					
·····	AH	5,546,596	8/1996	Geist	395	200.73	<u> </u>				
	AI	5,553,078	9/1996	Horie	370	389	<u> </u>				
	AJ	5,577,029	11/1996	Lu et al.	379	59					
	AK	5,583,990	12/1996	Birrittella et al.	395	800.29					
	AL	5,606,551	2/1997	Kartalopoulous	370	406					
	AM	5,617,413	4/1997	Monacos	370	400					
	AN	5,996,020	11/1999	Reed	709	238					
	<u> </u>	I	Foreign	Patent Documents	I	.,,,,_,,					
							Tra	nslatio			
		Document	Date	Country	Class	Subclass	Yes	No			
•	AO	94 12939	6//1994	WIPO							
	AP	95 16240	6/1995	WIPO							
· <u>···</u>		OTHER A	ART (Including A	Author, Title, Date, Pert	inent Pages, E	tc.)		·I			
	AQ	Aruna V. Ramanan, "Ultrafast Space-Time Networks for Multiprocessors", a thesis, 1993, pp. 1-170									
	AR	Malek M., et al.: "The Cylindrical Banyan Multicomputer: A Reconfigurable Systolic Architecture", May 1, 1989, pp. 319-327, Parallel Computing, XP000065558									
	AS	Isaac Yi-Yuan Lee et al.: "A Versatile Ring-Connected Hypercube", Jun. 1, 1994, pp. 60-67, IEEE Micro., pp. 60-67, XP000448657									
Examiner	-1-		Date Considere	ed				-			

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U.S. Departme	ent of Co	mmerce, Patent and	Atty Docket No.		Serial No.						
					M-3240-31	Not yet assigned					
INFO	RMATIC	N DISCLOSURE	Applicant								
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			U.S. Pate	ent Documents							
*Examiner Initial		Document Number	Date ·	Name	Class	Subclass	Filing Date If Appropriate				
***	AA										
	AB										
	AC										
	AD										
	AE										
	AF		ļ								
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			Foreign Pa	itent Documents			1				
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		Document	Date	Country	Class	Subclass	Yes	N			
	AH							<u> </u>			
	AI										
		OTHER	ART (Including Auth	nor, Title, Date, Per	rtinent Pages, E	tc.)					
	AJ		y: "I/O Embedding in Parallel Processing,				eedings of the	e 198			
	AK	Catier: "Une architecture "hypercube".", Sep. 1986, pp. 59-64, Electronique Industrielle, XP002016776									
	AL	Welty: "Hypercube architectures", Jun. 19, 1986, pp. 495-501, AFIPS Conference Proceedings 1986 National Computer Conference, XP002016777									
	AM	Proceedings of the Third IEEE Symposium on Parallel and Distributed Processing (Cat. No. 91TH0396-2), Dallas, TX, USA, Dec. 2-5, 1991, ISBN 0-8186-2310-1, Los Alamitos, CA, USA, IEEE Compt. Soc. Press, USA, pp. 564-571									
	AN	Young, S.D., et al, "Adaptive Routing in Generalized Hypercube Architectures", IEEE Symposium, Dec. 2-5, 1991, pp. 564-571, XP002024983									
	AO		t al.: "Adaptive Rout (ay 1, 1993, pp. 12-16			onnection Net	works", Com	puter			
Examiner			Date Considered								
*EXAMINER			Date Considered ered, whether or not considered. Include copy								